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**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of : Jigang Liu  
Application No. : 10/500,620  
Filed : July 1, 2004  
For : TRANSCEIVER WITH MULTI-STATE DIRECT DIGITAL  
SYNTHESIZER DRIVEN PHASE LOCKED LOOP  
Art Unit : 2618  
Examiner : Nguyen, Tuan Hoang  
Confirmation No. : 4330

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

**INFORMATION DISCLOSURE STATEMENT**

Pursuant to the duty of disclosure under 37 C.F.R. § 1.56, Applicant submits this statement. This submittal is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and § 609 of the Manual of Patent Examining Procedure. The reference herein is listed below and on the attached Form PTO/SB/08A. A copy of the listed reference is submitted herewith.

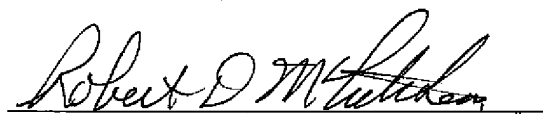
<u>Foreign Patent No.</u>	<u>Country</u>	<u>Date</u>
JP 61-285830	Japan	Dec. 16, 1986

Applicant hereby expressly reserves the right to swear behind the effective dates of any of the above Patents and to question the relevance and materiality of the Patents and Publications listed herein, in whole, in part, or in combination, subsequent to filing this Information Disclosure Statement.

This Information Disclosure Statement is being transmitted after the mailing date of the first Office Action on the merits. Therefore, the Commissioner is hereby authorized to charge \$180.00 for the Information Disclosure Statement filing fee any additional fees connected with this communication or credit any overpayment to Deposit Account No.

Respectfully submitted,

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